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PTO/SB/82 (10-00)

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Application Number	09/656,541
Filing Date	09-06-00
First Named Inventor	Beausoleil, W.F., et al
Group Art Unit	2123
Examiner Name	Not Yet Assigned
Attorney Docket Number	272/207

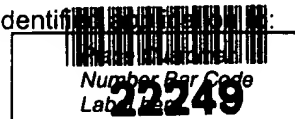
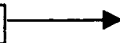
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☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Certificate under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

SIGNATURE of Applicant or Assignee of Record

Name R. L. Smith McKeithen, Secretary

Signature

Date

April 22, 2002

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required.
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Application Number	09/656,541
Filing Date	09-06-00
First Named Inventor	Beausoleil, W.F., et al
Group Art Unit	2123
Examiner Name	Not Yet Assigned
Attorney Docket Number	272/207

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SIGNATURE of Applicant or Assignee of Record

Name R.L. Smith McKeithen, Secretary, Cadence Design Systems, Inc.

Signature

Date

April 22, 2002

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Patent
Attorney Docket: 272/207

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

William F. Beausoleil, et al.

Serial No.: 09/656,541

Filed: September 6, 2000

For: High Speed Software Driven Emulator
Comprised Of A Plurality Of Emulation
Processors With A Method To High Speed
Bulk Read/Write Operation Synchronous
DRAM While Refreshing Memory

Group Art Unit: 2123

Examiner: Not Yet Assigned

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I. DOCUMENTS ENCLOSED:

- ☒ Statement Under 37 CFR 3.73(b) with Exhibit 1 and 2 attached
- ☒ Power of Attorney or Authorization of Agent

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(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Date of Deposit

Jeffrey A. Miller

Name of Person Mailing Paper

Signature of Person Mailing Paper

☒ The Commissioner is authorized to charge Lyon & Lyon's Deposit Account No. **12-2475** for any fees required under 37 CFR §§ 1.16, 1.17 and 1.445 and to credit any overpayments to said Deposit Account No. **12-2475**.

Respectfully submitted,

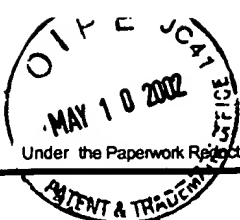
LYON & LYON LLP

Dated: April 26, 2002

By: 

Jeffrey A. Miller
Reg. No. 35,287

633 West Fifth Street, Suite 4700
Los Angeles, California 90071-2066
(408) 993-1555



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PTO/SB/96 (08-00)

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Quicktum Design Systems, Inc.

Application No./Patent No.: 09/656,541

Filed/Issue Date: 09-06-00

Entitled: High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with a Method to Allow High Speed Bulk read/Write Operation Synchronous DRAM While Refreshing Memory.

Quicktum Design Systems, Inc.

a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of an undivided part interest

in the patent application/patent identified above by virtue of a chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: William F. Beausoleil, Bryan R. Cook, Tak-Kwong Ng, Helmut Roth, Peter Tannenbaum, Lawrence A. Thomas and Norton J. Tomassetti

To: International Business Machines Corporation

The document was recorded in the United States Patent and Trademark Office at Reel 011148, Frame 0165, or for which a copy thereof is attached.

2. From: International Business Machines Corporation To: Cadence Design Systems, Inc. A copy of a document evidencing this assignment is being recorded herewith. A copy of this document is attached as Exhibit 1.

3. From: Cadence Design Systems, Inc. To: Quicktum Design Systems, Inc. A copy of a document evidencing this assignment is being recorded herewith. A copy of this document is attached as Exhibit 2.

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[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

April 22, 2002

Date

R.L. Smith McKeithen
Signature

R.L. Smith McKeithen

Typed or printed name

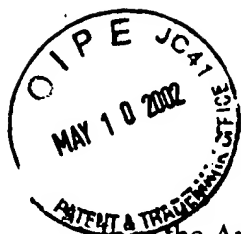
Secretary

Title

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2123

Patent
Attorney Docket: 272/207



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

William F. Beausoleil, et al.

Serial No.: 09/656,541

Filed: September 6, 2000

For: High Speed Software Driven Emulator
Comprised Of A Plurality Of Emulation
Processors With A Method To High Speed
Bulk Read/Write Operation Synchronous
DRAM While Refreshing Memory

)
) **Group Art Unit:** 2123

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) **Examiner:** Not Yet Assigned

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- ☒ Statement Under 37 CFR 3.73(b) with Exhibit 1 attached
- ☒ Revocation of Power of Attorney or Authorization of Agent
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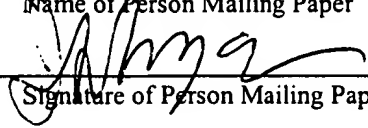
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Jeffrey A. Miller

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Respectfully submitted,

LYON & LYON LLP

Dated: April 26, 2002

By: 

Jeffrey A. Miller
Reg. No. 35,287

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